

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	186	(integrated near5 bus) same packet	USPA T; US-P GPUB	2004/10/1 7 21:35	
2	BRS	L2	51393	differential near3 (data or signal)	USPA T; US-P GPUB	2004/10/1 7 21:36	
3	BRS	L3	9	1 and 2	USPA T; US-P GPUB	2004/10/1 7 21:36	
4	BRS	L4	9488	2 and serial	USPA T; US-P GPUB	2004/10/1 7 21:36	
5	BRS	L5	3114	communication\$1 adj (riser or card or board)	USPA T; US-P GPUB	2004/10/1 7 21:39	
6	BRS	L6	99	4 and 5	USPA T; US-P GPUB	2004/10/1 7 21:40	
7	BRS	L7	3611	control adj2 slot	USPA T; US-P GPUB	2004/10/1 7 21:40	
8	BRS	L8	4385	data adj2 slot	USPA T; US-P GPUB	2004/10/1 7 21:41	
9	BRS	L9	524	7 and 8	USPA T; US-P GPUB	2004/10/1 7 21:41	
10	BRS	L10	0	6 and 9	USPA T; US-P GPUB	2004/10/1 7 21:41	
11	BRS	L11	3	5 and 9	USPA T; US-P GPUB	2004/10/1 7 21:48	
12	BRS	L12	21	4 and 9	USPA T; US-P GPUB	2004/10/1 7 22:01	
13	BRS	L13	1186	710/1-2.ccls.	USPA T; US-P GPUB	2004/10/1 7 22:01	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	486	710/20-21.ccls.	USPA T; US-P GPUB	2004/10/1 7 22:01	
15	BRS	L15	2815	710/100.ccls. 710/105.ccls. 710/305.ccls.	USPA T; US-P GPUB	2004/10/1 7 22:02	
16	BRS	L16	976	710/313.ccls. 710/315.ccls. 710/316.ccls.	USPA T; US-P GPUB	2004/10/1 7 22:03	
17	BRS	L17	5063	13 or 14 or 15 or 16	USPA T; US-P GPUB	2004/10/1 7 22:04	
18	BRS	L18	889	differential\$2 near3 (encod\$3 and decod\$3)	USPA T; US-P GPUB	2004/10/1 7 22:05	
19	BRS	L19	551	2 and 18	USPA T; US-P GPUB	2004/10/1 7 22:05	
20	BRS	L20	2	17 and 19	USPA T; US-P GPUB	2004/10/1 7 22:06	
21	BRS	L21	6	9 and 17	USPA T; US-P GPUB	2004/10/1 7 22:06	